## Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | PARTSCH, TORSTEN | Examiner | Art Unit | Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
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	Е	US-			
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	Ι	US-			
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	J	US-			
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	L	US-	14.		
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## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country ,	Name	Classification
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	.R					
	S					
	Т					

## **NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)						
******	U	Sakamoto et al. "A Digitally Programmable Delay Chip with Picosecond Resolution". 19 September 1989. IEEE. Bipolar Circuits and Technology Meeting, 1989., Proceedings of the 1989. pp. 295-297.					
	v						
	w						
	х						

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